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(Not for submission under 37 CFR 1.99)

Application Number	10602292
Filing Date	2003-06-24
First Named Inventor	Michael B. Doerr
Art Unit	2181
Examiner Name	Meonske, Tonia L.
Attorney Docket Number	5860-00101

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	3	Taylor et al., "The Raw Microprocessor: A Computational Fabric for Software Circuits and General-Purpose Programs," IEEE Micro, March-April 2002, 11 pages.	☐
	4	Lee et al., "Space-Time Scheduling of Instruction-Level Parallelism on a Raw Machine," Proceedings of the Eighth International Conference on Architectural Support for Programming Language and Operating Systems (ASPLOS-8), October, 1998, 11 pages.	☐

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	5	Barua et al., "Compiler Support for Scalable and Efficient Memory Systems," IEEE Transactions on Computers, November 2001, 32 pages.	☐
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	10	Frank et al., "SUDS: Primitive Mechanisms for Memory Dependence Speculation," MIT/LCS Technical Memo LCS-TM-591, January 6, 1999, 9 pages.	☐
	11	Barua, "Maps: A Compiler-Managed Memory System for Software-Exposed Architectures," PhD Thesis, MIT Laboratory for Computer Science, January 2000, 161 pages.	☐
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	14	Moritz, et al., "Hot Pages: Software Caching for Raw Microprocessors," MIT/LCS Technical Memo LCS-TM-599, August, 1999, 12 pages.	☐
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